

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: JOSEPH HUANG ET AL.

Filed: JANUARY 15, 2004

For: APPARATUS AND METHODS FOR PROVIDING
REDUNDANCY IN INTEGRATED CIRCUITS

Serial No.: 10/757,928

Group Art Unit: 2827

Examiner: WEINBERG, MICHAEL J.

Atty Dkt: ALTR:023

Pursuant to 37 C.F.R. § 1.8, I certify that this correspondence is being filed electronically, via facsimile, or deposited with the U.S. Postal Service in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date below:

July 20, 2006
Date


Name

REQUEST FOR AN EXTENSION OF TIME

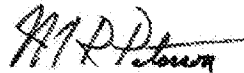
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. § 1.136(a), Applicants petition for an extension of time for one month(s) to and including July 21, 2006, in which to respond to the Office Action dated March 21, 2006.

Concurrently with filing this paper, the Applicant provides payment electronically. If any other fees are due for any of the enclosed materials, please deduct such fees from (or credit any overpayment to) deposit account number 50-3813/ALTR-023.

Respectfully submitted,



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